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CRW  
2121

Docket No.: CIS0115US

July 29, 2005

Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P. O. Box 1450  
Alexandria, VA 22313-1450

Re: Applicants: Christopher H. Pham  
Assignee: Cisco Technology, Inc.  
Title: LINEAR ASSOCIATIVE MEMORY-BASED HARDWARE ARCHITECTURE FOR  
FAULT TOLERANT ASIC/FPGA WORKAROUND  
Serial No.: 09/837,882  
Examiner: Mai T. Tran  
Docket No.: CIS0115US

Filed: April 18, 2001  
Group Art Unit: 2121

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (1 page) (*in duplicate*); and
- (3) Response To Non-Final Office Action (6 pages).



No additional fee is required.



The fee has been calculated as shown below:

**CLAIMS AS AMENDED**

	Claims Remaining <u>After Amendment</u>		Highest No. <u>Previously Paid For</u>		Present <u>Extra</u>	x	<u>Rate</u>		Additional <u>Fee</u>
Total Claims	42	Minus	42	=	0	x	\$ 50.00	\$	0.00
Independent Claims	4	Minus	4	=	0	x	\$200.00	\$	0.00
<input type="checkbox"/> Fee for Request for Extension of Time						x		\$	<u>0.00</u>
<b>Total additional fee for this Amendment:</b>								\$	<u>0.00</u>

- Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- Please charge our Deposit Account No. 502306 in the amount of \$ 0.00
- Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 502306.

**Total:** \$ 0.00

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on July 29, 2005.

Attorney for Applicants

7/29/05

Date of Signature

Respectfully submitted,

Marc R. Ascolese  
Attorney for Applicants  
Reg. No. 42,268



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Christopher H. Pham  
Assignee: Cisco Technology, Inc.  
Title: Linear Associative Memory-Based Hardware Architecture For Fault Tolerant ASIC/FPGA Workaround  
Serial No.: 09/837,882 Filing Date: July 22, 1999  
Examiner: Mai T. Tran Group Art Unit: 2129  
Docket No.: CIS0115US Client Ref. No.: 3001

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Austin, Texas  
July 29, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NON-FINAL OFFICE ACTION**

Dear Sir:

This paper is responsive to the non-final Office action dated July 14, 2005, having a shortened statutory period set to expire October 14, 2005. Further examination and consideration are requested.

No Amendments to the Specification are presented in this paper.

No Amendments to the Claims are presented in this paper.

No Amendments to the Drawings are presented in this paper.

Remarks begin on page 2 of this paper.